Learn Digital Design with PSoC, a bit at a time

By Dave Van Ess

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Whether you're an engineering student or looking to get into digital design as a hobby, there has never been a better time to start building. The wealth of easily available parts and resources means anybody can get started understanding logic components, tinkering with computer hardware, and developing circuitry, with or without formal training in engineering.

The secret? Take it a bit at a time, and get comfortable with not knowing what you're doing. This is the vision behind *Learn Digital Design with PSoC, a Bit at a Time*, a straightforward and hands-on approach to building with PSoC (Programmable System on a Chip). Through simple instructions and plenty of hands-on labs, readers will ease their way into the world of digital design and building, while learning the subject matter in easy-to-digest pieces.

In the vein of Blaine Readler and Don Lancaster, author Dave Van Ess draws upon his years of engineering experience to provide a fun and non-intimidating beginner's guide to circuitry. Maker Movement hobbyists, prospective engineers, and grad students alike will enjoy this laid-back yet productive approach to building with PSoC, starting off simple and slowly progressing to more and more complex circuitry.

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Editorial Review

About the Author

Dave Van Ess is a graduate of the University of California, Berkeley, and an electrical engineer with more than thirty-five years of experience in hardware, software, and analog design.

Throughout his career in several different industries, he has built test and measurement equipment and control systems for high-energy physics research, as well as acoustical equipment to be used underwater in the open sea and arctic ice fields.

A self-described engineer by training, poet by temperament, and a pain in the side of management, he has a wife of forty years, two children, and a new granddaughter with whom he looks forward to building things with Legos.

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